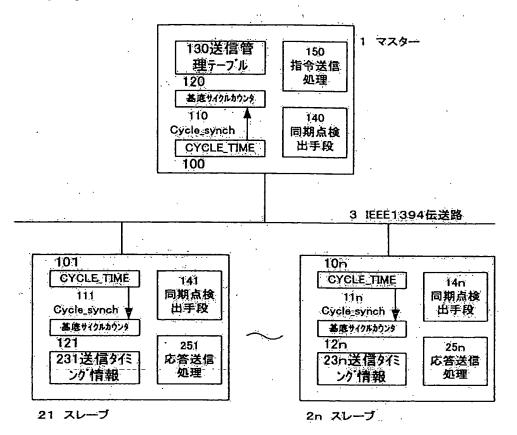
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Fig. 2

{図1}~



·1图27 Fig. Z

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0	全サイクル数	m+1			
3	サイクルカウンタ	O	1	T7	m ⁻
3	送信指令数	2	2	T	2
(4)	送信先	#1	#3		#n-1
9	スレーブNo.	#2	#4		#n-1 #n
				<u> </u>	

スレーブ送信タイミング情報 231 ヘレーブ送信タイミング情報 23n

9	全サイクル数	m+1
(0)	指令サイクル	o
Ó	応答サイクル	0

3	全サイクル数	m+1
<u></u>	指令サイクル	m
(7)	応答サイクル	m
-		

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Fみ.3 .【図3】

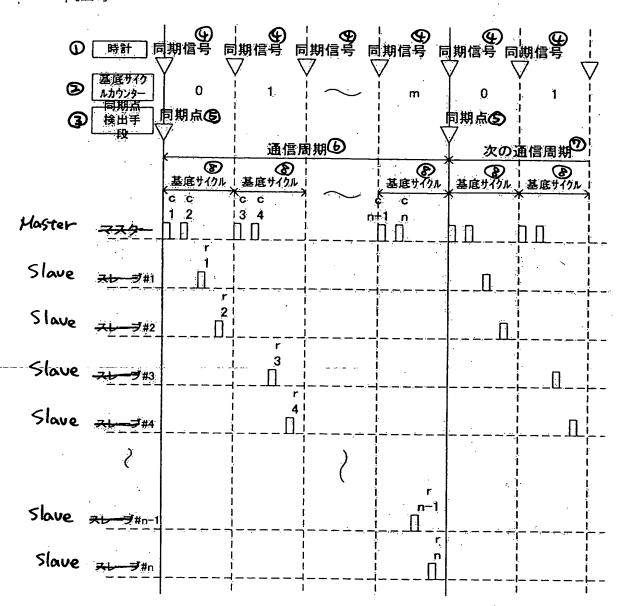
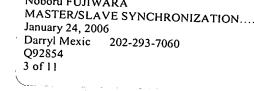


Fig. 4 -[図4] -



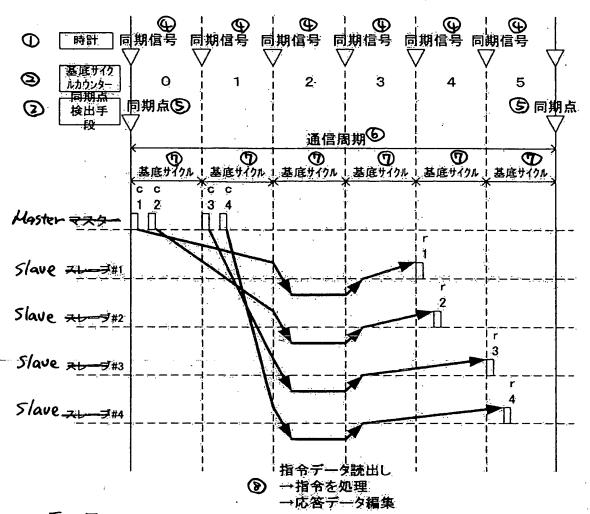


Fig. 5 {図5}

second_count	cycle_count	cycle_offset
(7bit)	(13bit)	(12bit)

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下》. 6

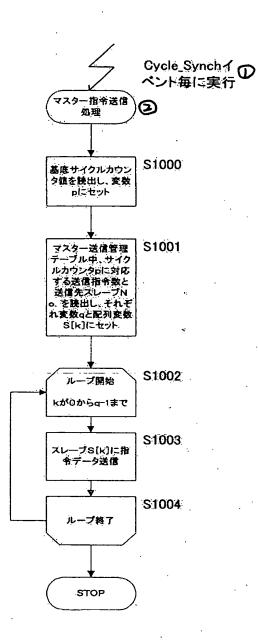
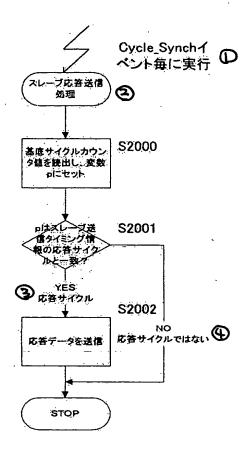


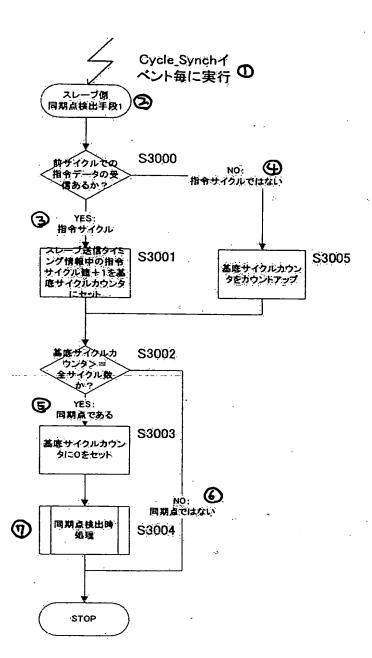
Fig. 7

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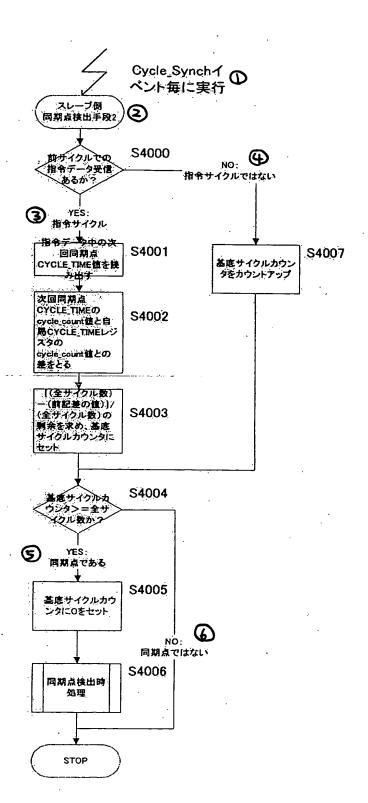
Fig. 8 [図8]-



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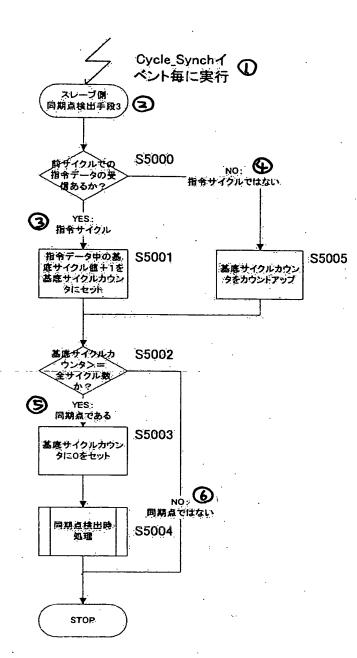
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Fig.9



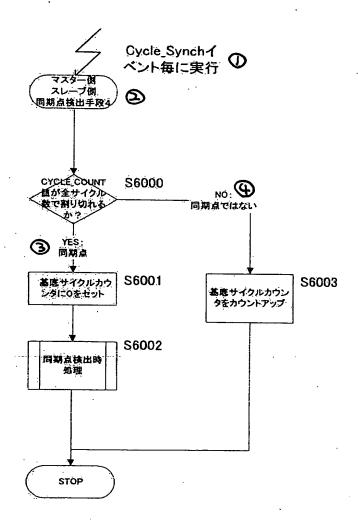
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Fig. 10 -[図10]−



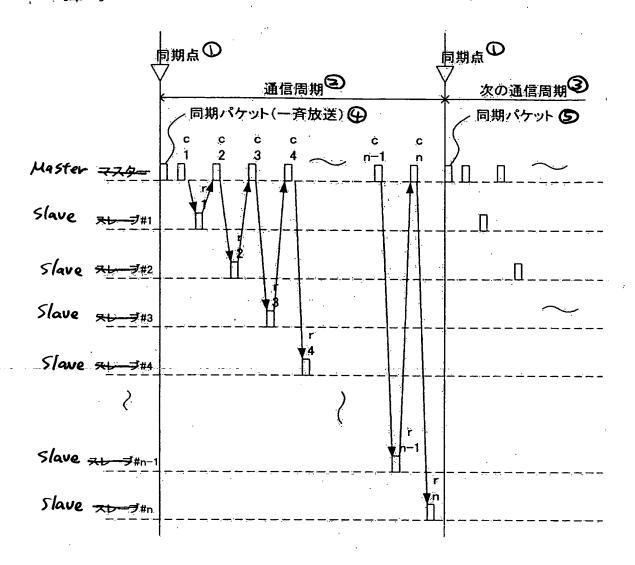
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Fig. 11



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Fix.1Z



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Fig. 13 ,[図13]-

